

FIG. 2

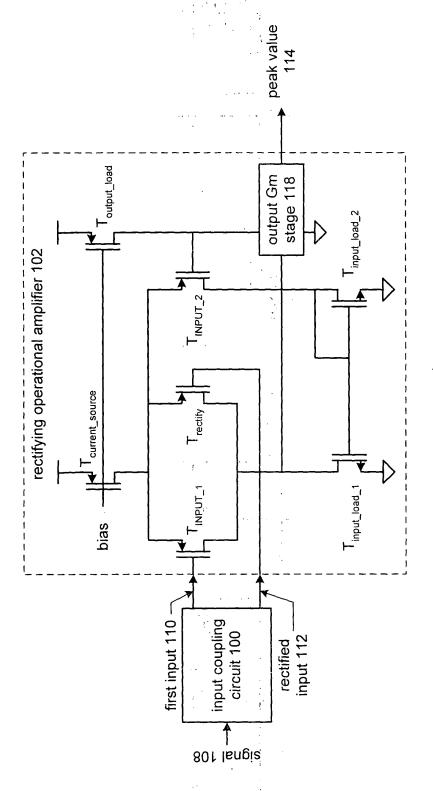


FIG. 3 signal peak detect circuit 95

